

15This Page Is Inserted by IFW Operations  
and is not a part of the Official Record

## **BEST AVAILABLE IMAGES**

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images may include (but are not limited to):

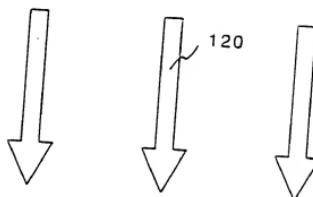
- BLACK BORDERS
- TEXT CUT OFF AT TOP, BOTTOM OR SIDES
- FADED TEXT
- ILLEGIBLE TEXT
- SKEWED/SLANTED IMAGES
- COLORED PHOTOS
- BLACK OR VERY BLACK AND WHITE DARK PHOTOS
- GRAY SCALE DOCUMENTS

**IMAGES ARE BEST AVAILABLE COPY.**

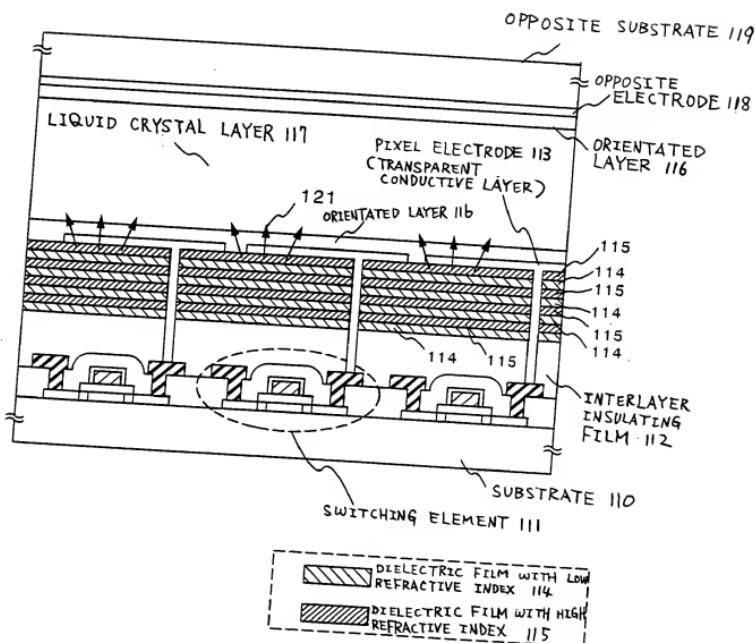
**As rescanning documents *will not* correct images,  
please do not report the images to the  
Image Problem Mailbox.**

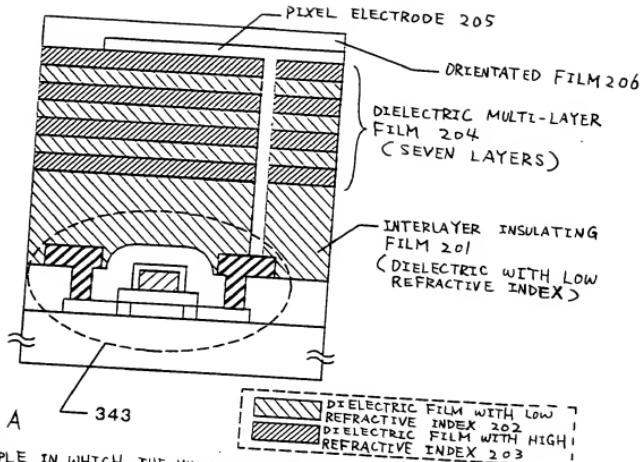
INCIDENT LIGHT

Fig. 1

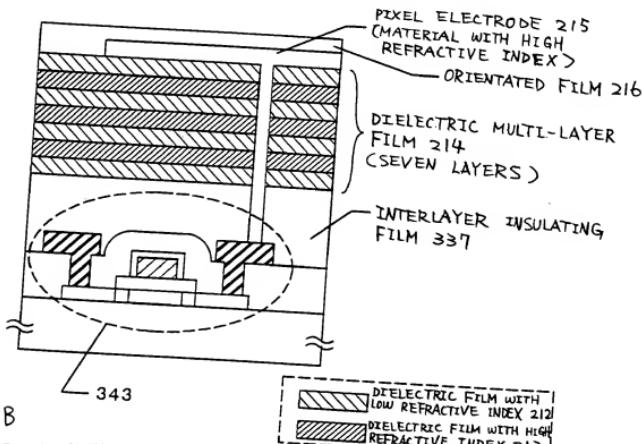


30  
31  
32  
33  
34  
35  
36  
37  
38  
39  
40  
41  
42  
43  
44  
45  
46  
47  
48  
49  
50  
51  
52  
53  
54  
55  
56  
57  
58  
59  
60  
61  
62  
63  
64  
65  
66  
67  
68  
69  
70  
71  
72  
73  
74  
75  
76  
77  
78  
79  
80  
81  
82  
83  
84  
85  
86  
87  
88  
89  
90  
91  
92  
93  
94  
95  
96  
97  
98  
99  
100





EXAMPLE IN WHICH THE NUMBER OF LAYERS IS REDUCED BY USING DIELECTRIC WITH LOW REFRACTIVE INDEX FOR INTERLAYER INSULATING FILM



EXAMPLE IN WHICH THE NUMBER OF LAYERS IS REDUCED BY USING PIXEL ELECTRODE (ITO) AS MATERIAL WITH HIGH REFRACTIVE INDEX

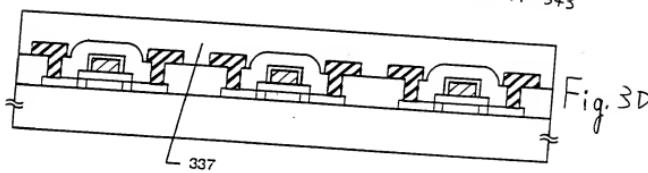
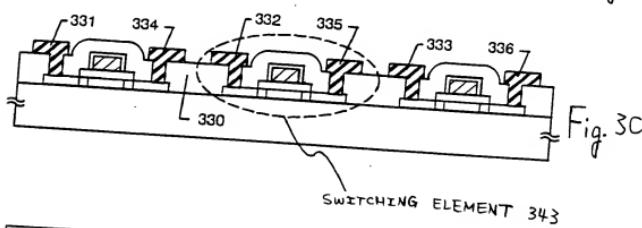
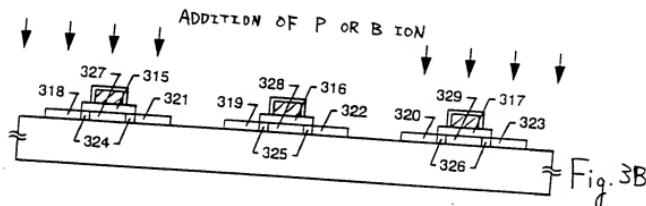
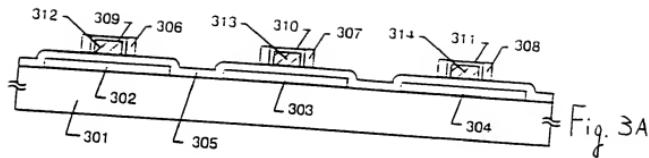


Fig. 4A

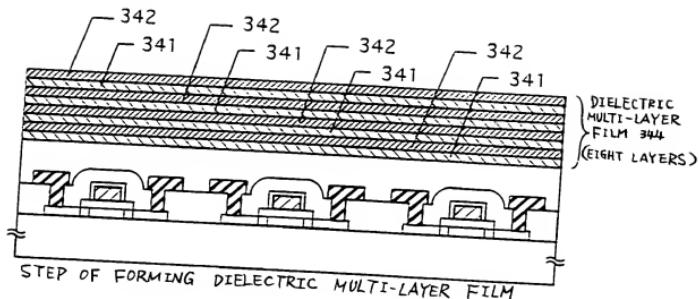


Fig. 4B

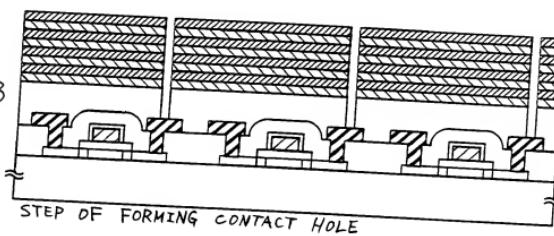
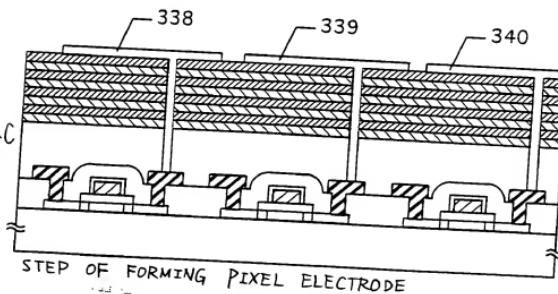


Fig. 4C



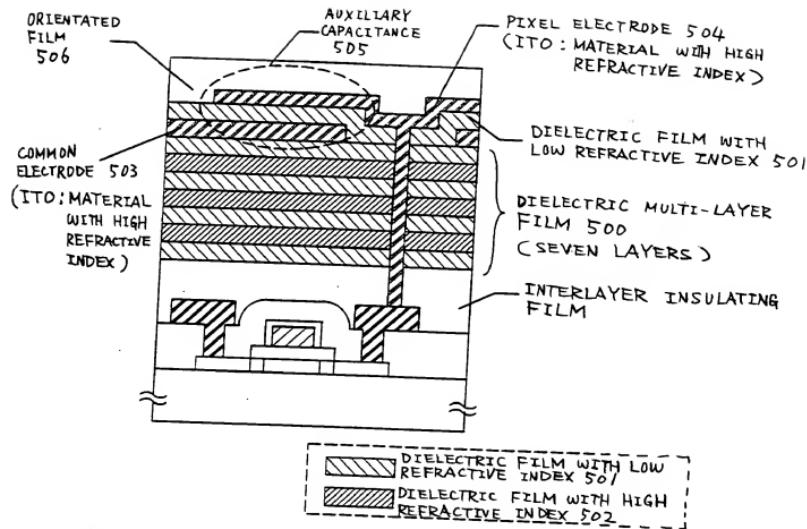


Fig. 5

Fig. 6A (CAPACITIVE CONFIGURATION 1)

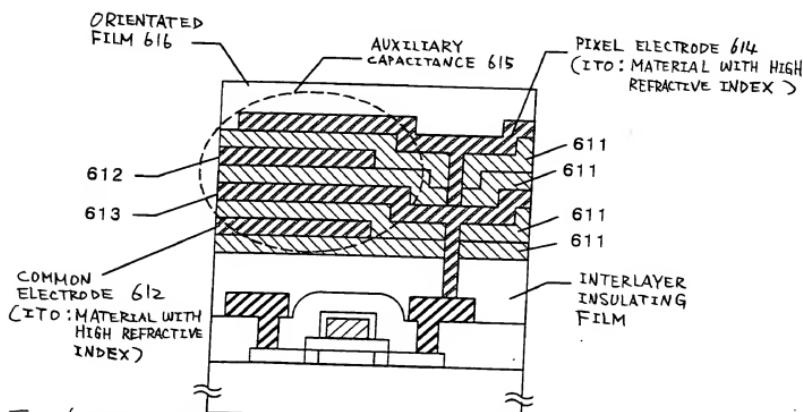
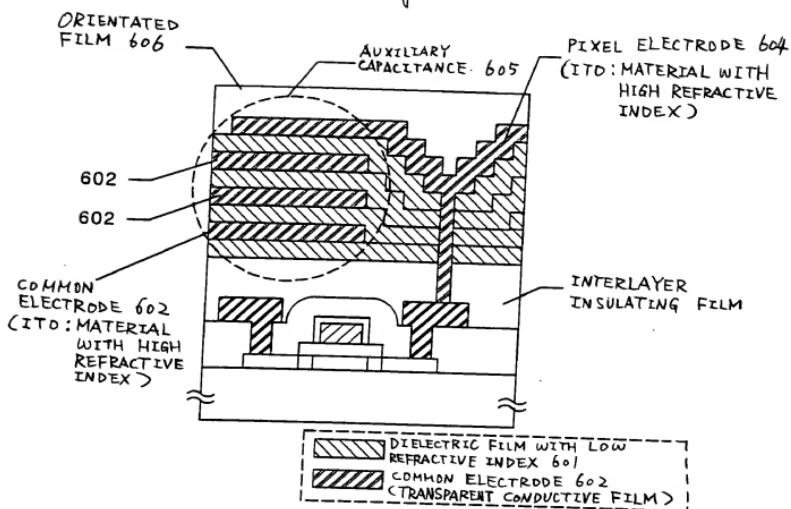


Fig. 6B (CAPACITIVE CONFIGURATION 2)

0  
9  
8  
7  
6  
5  
4  
3  
2  
1  
0

Fig. 7

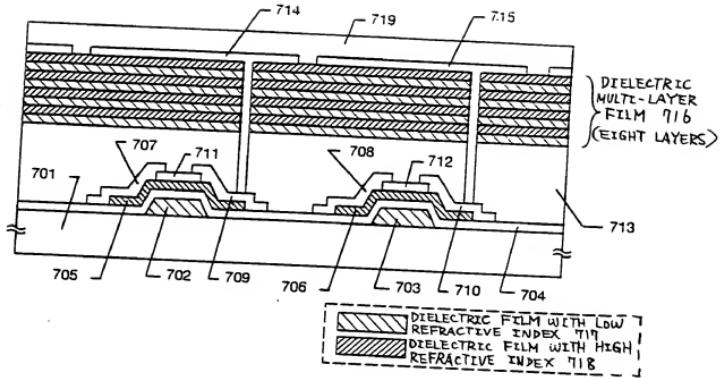
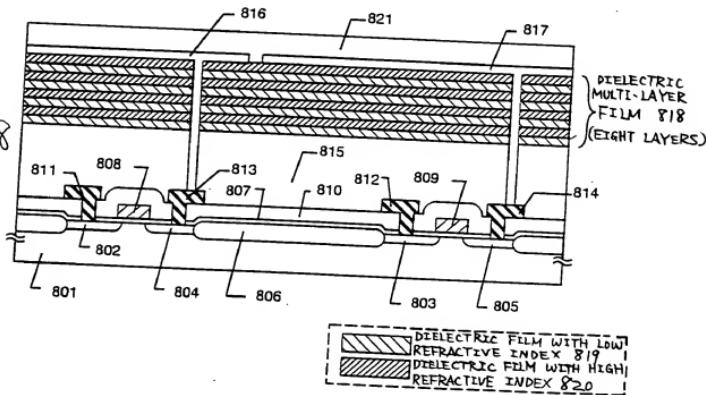


Fig. 8



665755-0206-0

INCIDENT LIGHT

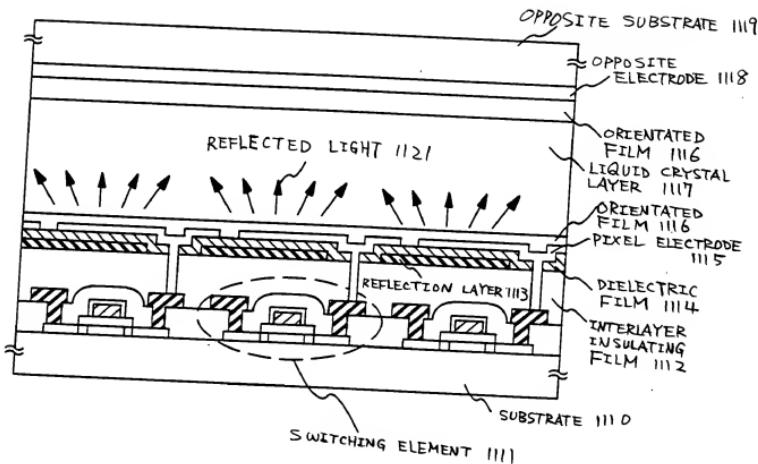
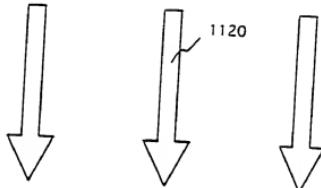
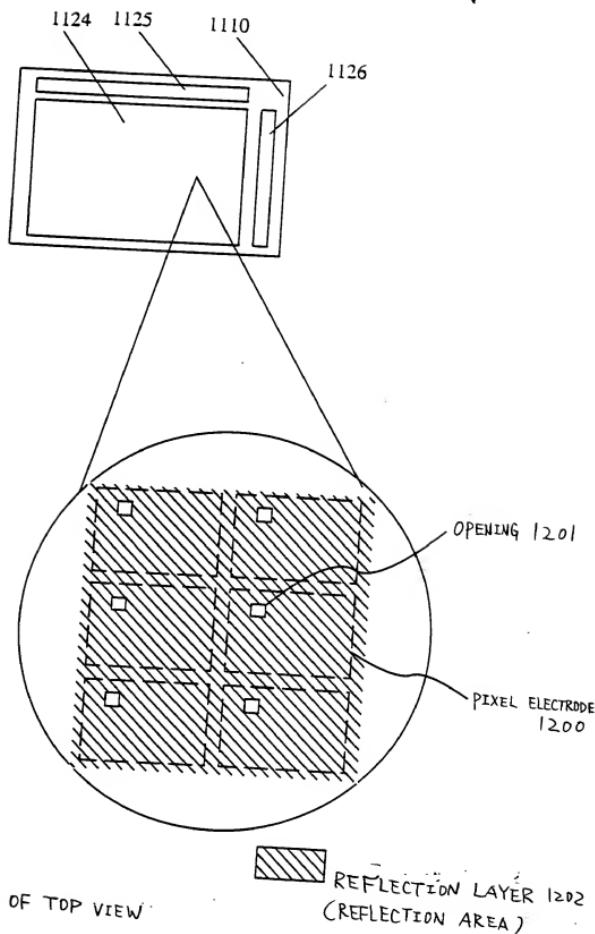
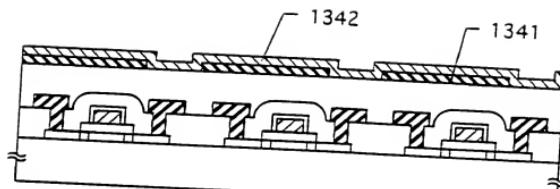


Fig. 9

Fig. 10

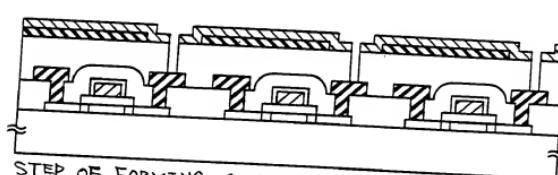
662750-040000





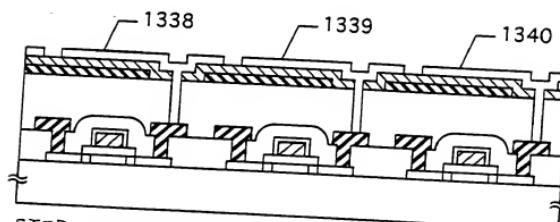
STEP OF FORMING REFLECTION LAYER AND DIELECTRIC FILM

Fig.11A



STEP OF FORMING CONTACT HOLE

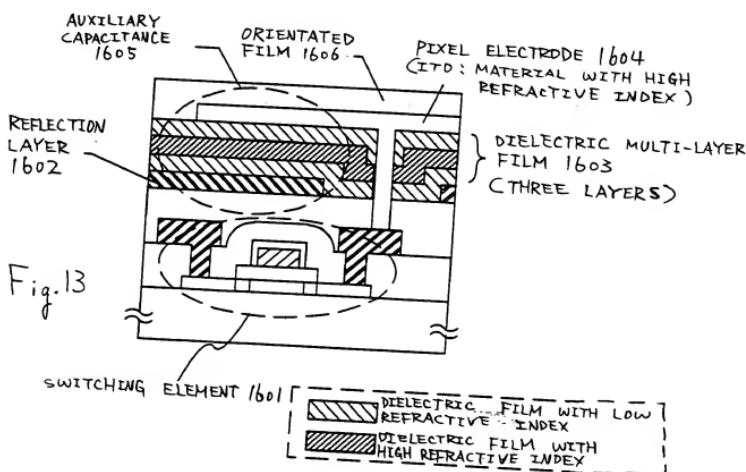
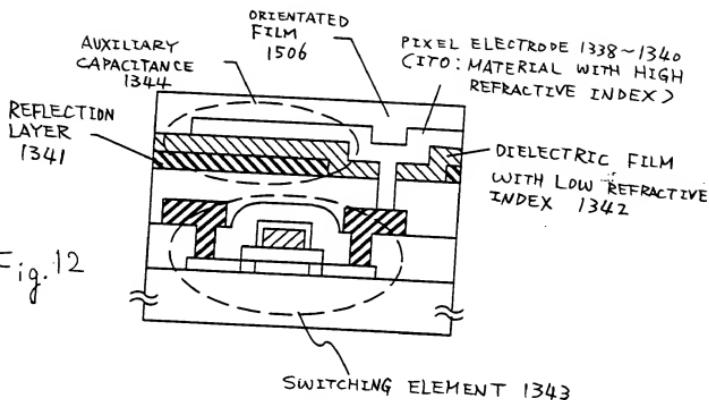
Fig.11B



STEP OF FORMING PIXEL ELECTRODE

Fig.11C

660750-0704599



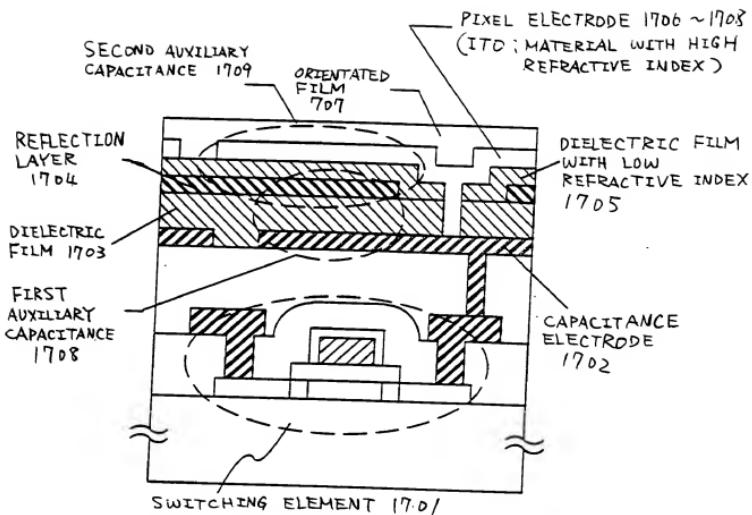
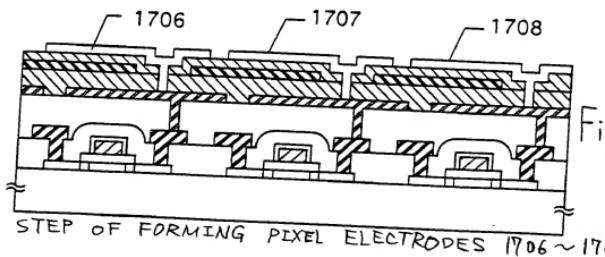
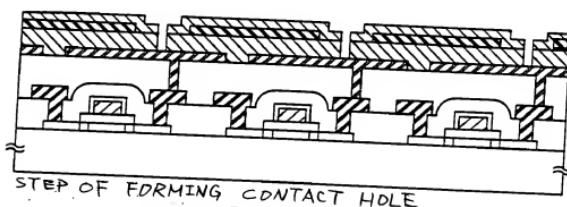
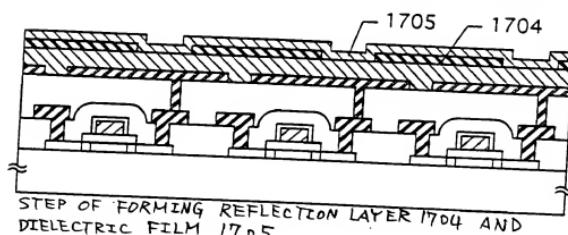
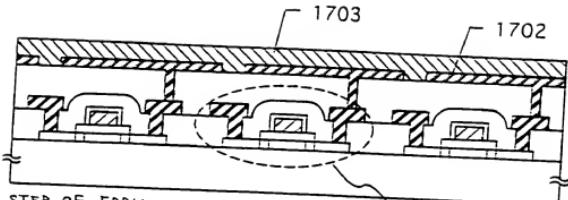


Fig. 14

0034407000



660450 644400 644400 644400 644400 644400

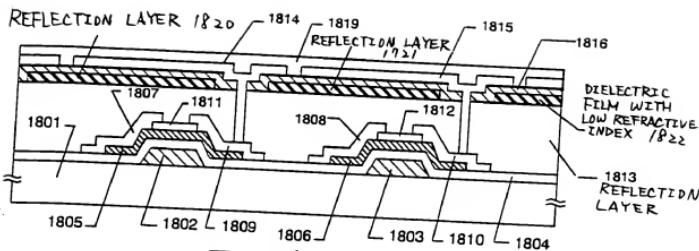


Fig. 16

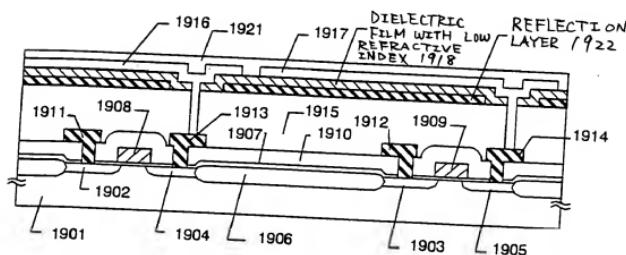


Fig. 17

Fig. 18A

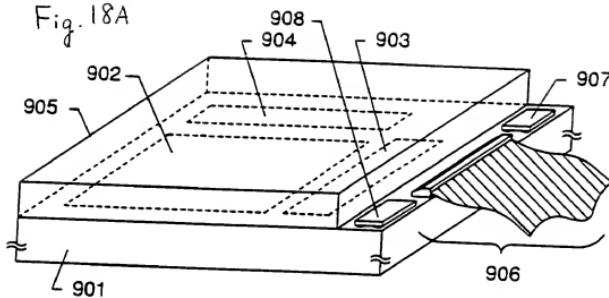
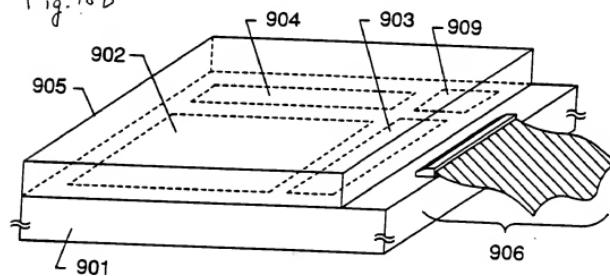
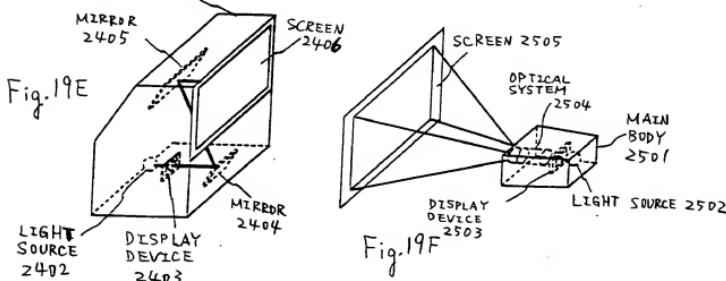
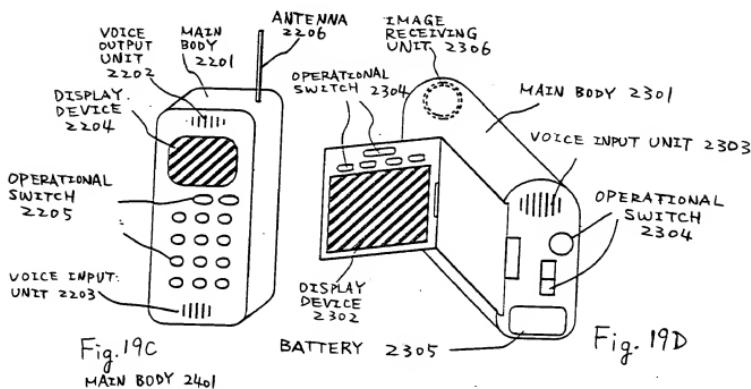
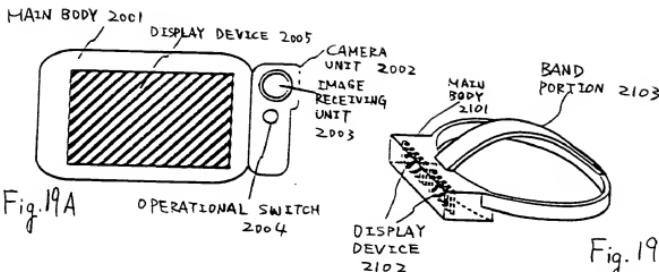


Fig. 18B

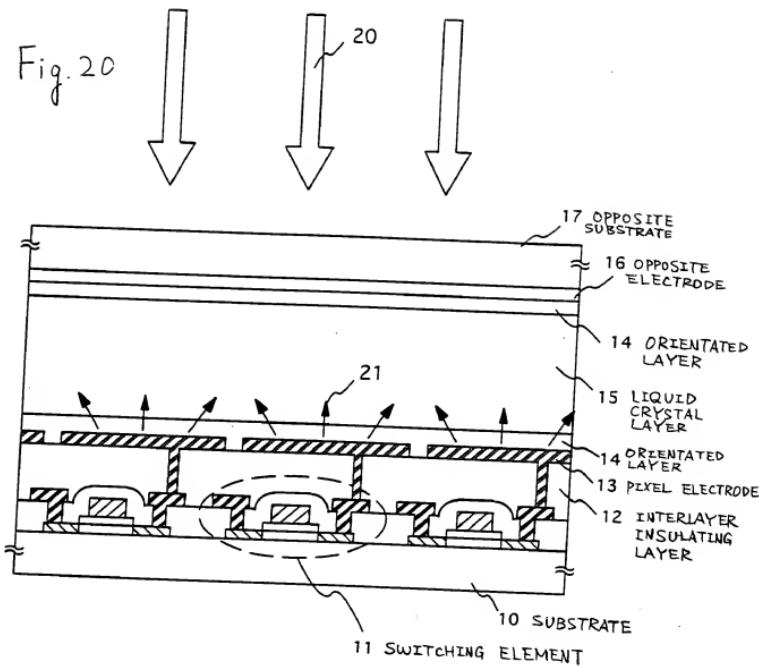


901: ACTIVE MATRIX SUBSTRATE    902: PIXEL MATRIX CIRCUIT  
903: SOURCE-SIDE DRIVER CIRCUIT    904: GATE-SIDE DRIVER CIRCUIT  
905: OPPOSING SUBSTRATE    906: FPC  
907, 908: IC CHIP    909: LOGIC CIRCUIT

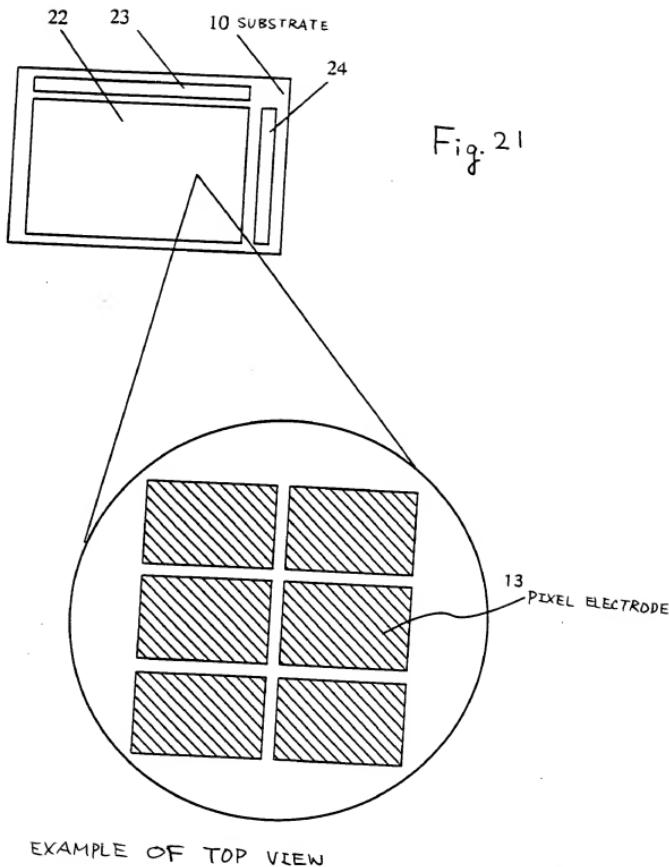


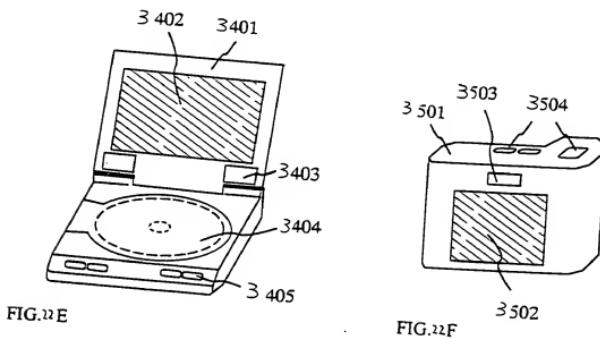
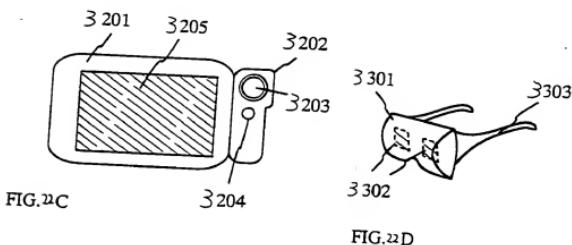
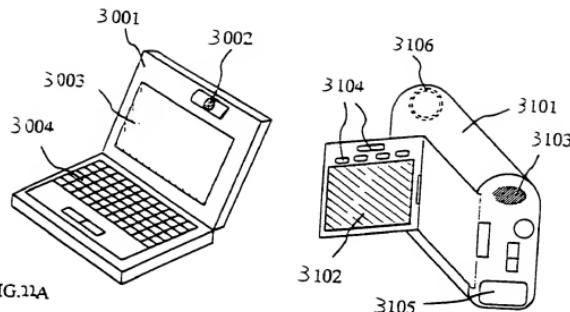
## INCIDENT LIGHT

Fig. 20



6557550-0255550





662750-02050

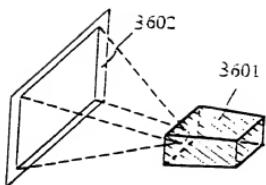


FIG. 23A

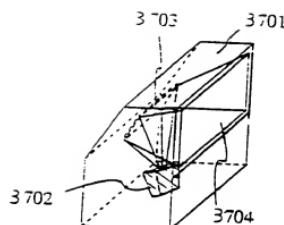


FIG. 23B

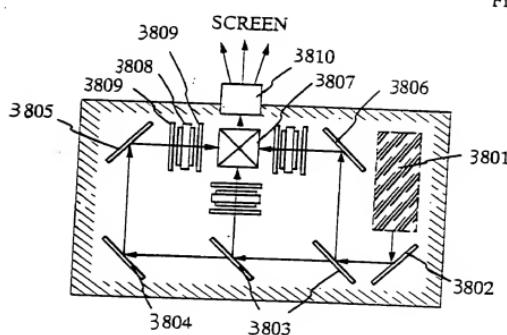


FIG. 23C

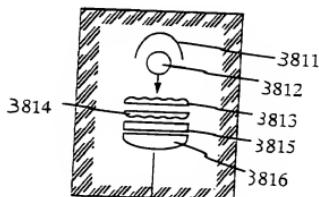


FIG. 23D

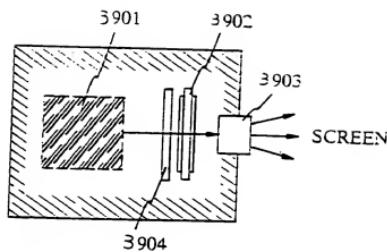


FIG. 24A

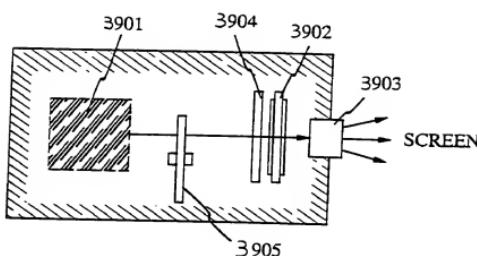


FIG. 24B

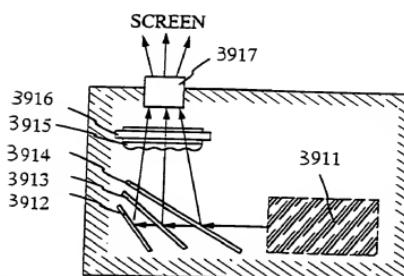


FIG. 24C